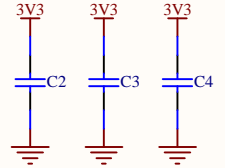
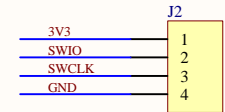
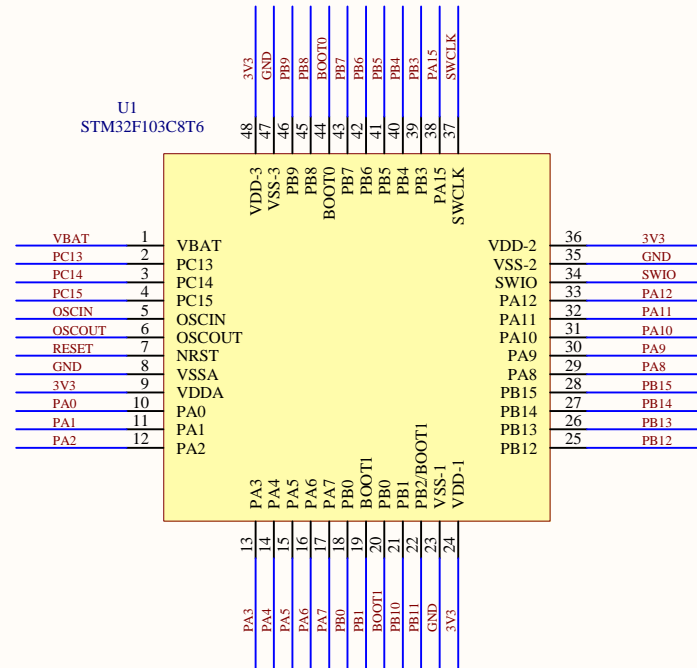
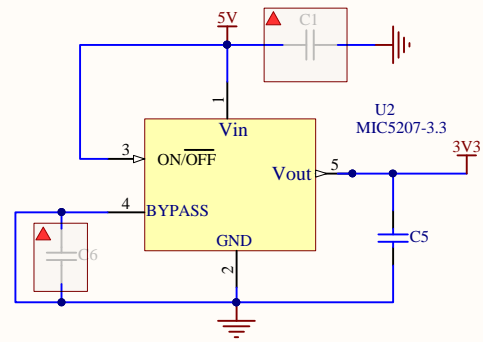


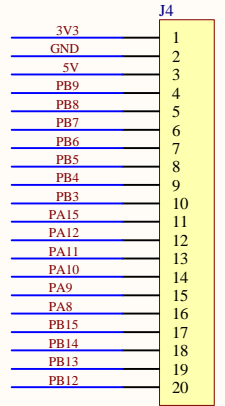
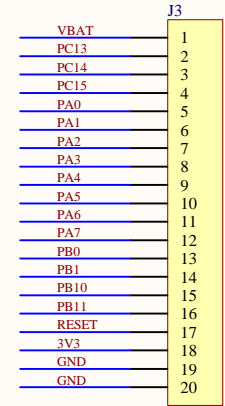
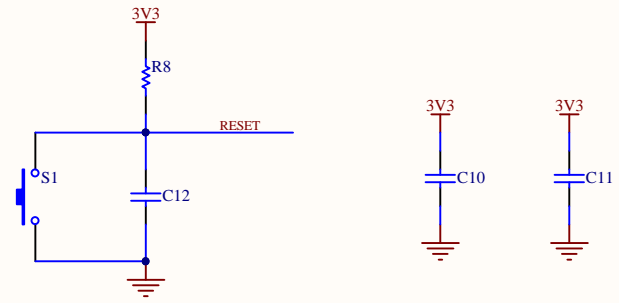
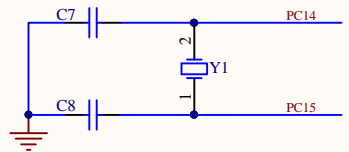
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A



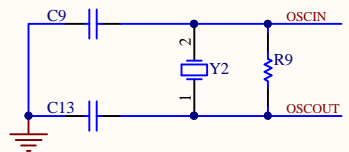
B

B



C

C



STM32 ARM Cortex-M3  
Minimum System

D

D